

## **ABSTRACT OF THE DISCLOSURE**

A drive circuit with a first voltage supply, and a second voltage supply that provides a voltage that is lower than the first voltage supply. The drive circuit also has a first transistor with either a drain or a source terminal connected to the first voltage supply, and a second transistor with either a drain or source terminal connected to the second voltage supply. A signal line is connected to each gate terminal of the first and second transistors, and at least one capacitance load is connected to respective terminals of the first and the second transistors that are not connected to the first and second voltage supplies. The signal line conveys signals having a high level that is substantially the same or higher than the voltage of the first voltage supply and a low level that is substantially the same or lower than the voltage of the second voltage supply.